Electronic Acknowledgement Receipt					
EFS ID:	1759070				
Application Number:	10796672				
International Application Number:					
Confirmation Number:	3015				
Title of Invention:	SEMICONDUCTOR DEVICE TEST PATTERNS AND RELATED METHODS FOR PRECISELY MEASURING LEAKAGE CURRENTS IN SEMICONDUCTOR CELL TRANSISTORS				
First Named Inventor/Applicant Name:	Young-pil Kim				
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Filer:	D. Randal Ayers/Michele McMahan				
Filer Authorized By:	D. Randal Ayers				
Attorney Docket Number:	5649-1276				
Receipt Date:	09-MAY-2007				
Filing Date:	09-MAR-2004				
Time Stamp:	14:22:19				
Application Type:	Utility				
Payment information:					
Submitted with Payment	no				

File Listing:

	Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	(if appl.)
	1	Post Allowance Communication - Incoming	5649_1276_CommentsAllow ance.pdf	100857	no	2
	Warnings:					
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## New Applications Under 35 U.S.C. 111

If a new application is being filled and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

## New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.